IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
Applicant(s): Manabe, Kazutaka	
Serial No.: N/A	Group Art Unit (of Parent Application): 2815
Filed: Herewith	Examiner: N/A
Title: Semiconductor Device and Its Manufacturing Method	
Attorney Docket No.: N09692401WD1	
PRELIMINARY AMENDMENT	
Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	
A. <u>Introductory Comments</u>	
The following is a Preliminary Amendment for the attached patent application.	
This response is presented in the revised amendment format set forth in 1265 Off. Gaz. Pat. Office 87 (Dec. 17, 2002) and so is subject to waiver of the provisions of 37 C.F.R. §1.21(a), (b) and (c).	
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